

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yijun Cai

Title: METHODS AND CIRCUITRY FOR REDUCING INTERMODULATION IN INTEGRATED

TRANSCEIVERS

Docket No.: 0033-10037897

Filed: October 22, 2001

Examiner: Alexander Jamal

Serial No.: 10/037,897

Due Date: March 27, 2006

Group Art Unit: 2614

Confirmation No.: 6855

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Notice of Allowance Date:

December 27, 2005

We are transmitting herewith the attached:

- X PTO-2038 Form (1 pg.)
- X Authorization to charge credit card to an amount \$1,400.00 to cover the Issue Fee Payment.
- X Authorization to charge credit card to an amount \$30.00 to cover the Extra Patent Copies Fee (10 copies).
- X Issue Fee Transmittal (Form PTOL-85).
- X Authorization to charge credit card to an amount \$300.00 to cover the Publication Fee Payment.
- X Comments on Statement of Reasons for Allowance Under 37 CFR § 1.104(e) (8 pages).
- X Notification of Change of Correspondence Address (1 page)
- X A return postcard.

Please charge any additional required fees for the Issue Fee Payment or credit overpayment to Deposit Account No. 50-3521.

William G. Hughes
Reg. No. 46,112

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn – MAIL STOP ISSUE FEE, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27 day of March, 2006.

Shellie Bailey

Signature

6/N 10/037,897 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yijun Cai

Examiner: Jamal, Alexander

Serial No.:

10/037,897

Group Art Unit: 2643

Filed:

April 4, 2001

Docket No.: 0033-10037897

Title:

Methods and Circuitry for Reducing Intermodulation in Integrated Transceivers

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE UNDER 37 CFR § 1.104(e)

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant has reviewed the Notice of Allowability mailed on <u>December 27, 2005</u> regarding the above-referenced application. Applicant respectfully submits Comments on the Statement of Reasons for Allowance as follows.

Claims 1-7, 10-15, and 17-29 are allowed. Claims 8, 9, 16, and 30-34 are canceled, and Applicant reserves the right to pursue subject matter of these claims in continuing applications.

Applicant respectfully acknowledges with gratitude the notification of allowance of claims 1-7, 10-15, and 17-29.

The Examiner states (Notice of Allowability mailed December 27, 2005, p. 5):

The following is an examiner's statement of reasons for allowance:

In the examiner's opinion, it would not have been obvious to a person of ordinary skill in the art to utilize one VCO on an integrated circuit as a frequency generator in order to derive the values of the equations taught in the Chang reference and further use those values to optimize the performance of both VCO's on the single IC chip.

Applicant respectfully submits that none of claims 1-7, 10-15, and 17-29 specifically recite the features discussed by the Examiner in the "statement of reasons for allowance." Applicant thus understands that the Examiner's statement merely acknowledges that "it would not have been obvious to a person of ordinary skill in the art" to try to modify the *Chang et al.* reference as suggested by the Examiner.

Applicant further notes that the Interview Summary indicating "30 November 2005" as a "Date of Interview" states:

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE UNDER 37 CFR § 1.104(e)

Serial Number: 10/037,897 Filing Date: October 22 2001

Title: Methods and Circuitry for Reducing Intermodulation in Integrated Transceivers

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Examiner contact [sic] Mr. Jeon to request that claims 30-34 be cancelled as they did not read over the prior art.

However, Applicant respectfully submits that claims 30-34 were canceled only in the interest of advancing prosecution to allowance and issue of the remaining claims, and does not agree that the features of claims 30-34 are suggested or disclosed by the applied references.

The Examiner is invited to telephone Applicant's attorney (703-286-5735) to facilitate issue of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 50-3521.

Respectfully submitted,

Brake Hughes PLC Customer Number 57246 Phone (703) 286-5735

Date March 27, 2006

William G. Hughes

Reg. No. 46,112

<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this <u>27</u> day of <u>March, 2006</u>.

Shellie Bailey

Stellie Bailey